



孕龍科技股份有限公司
Zeroplus Technology Co., Ltd.

SPECIFICATION

MODEL: 029-LAP-CCIR656-M

PART NO: _____

VERSION: V1.32

Approver		Check	Design
GM	PM		

Customer Confirm

*Please fax the file to Zeroplus Technology after signing.

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Content

1	Software Register	3
2	User Interface	5
3	Operating Instructions	7



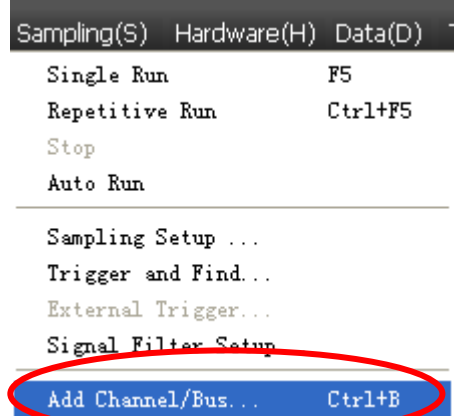
1 Software Register

Please register the software as the following steps:

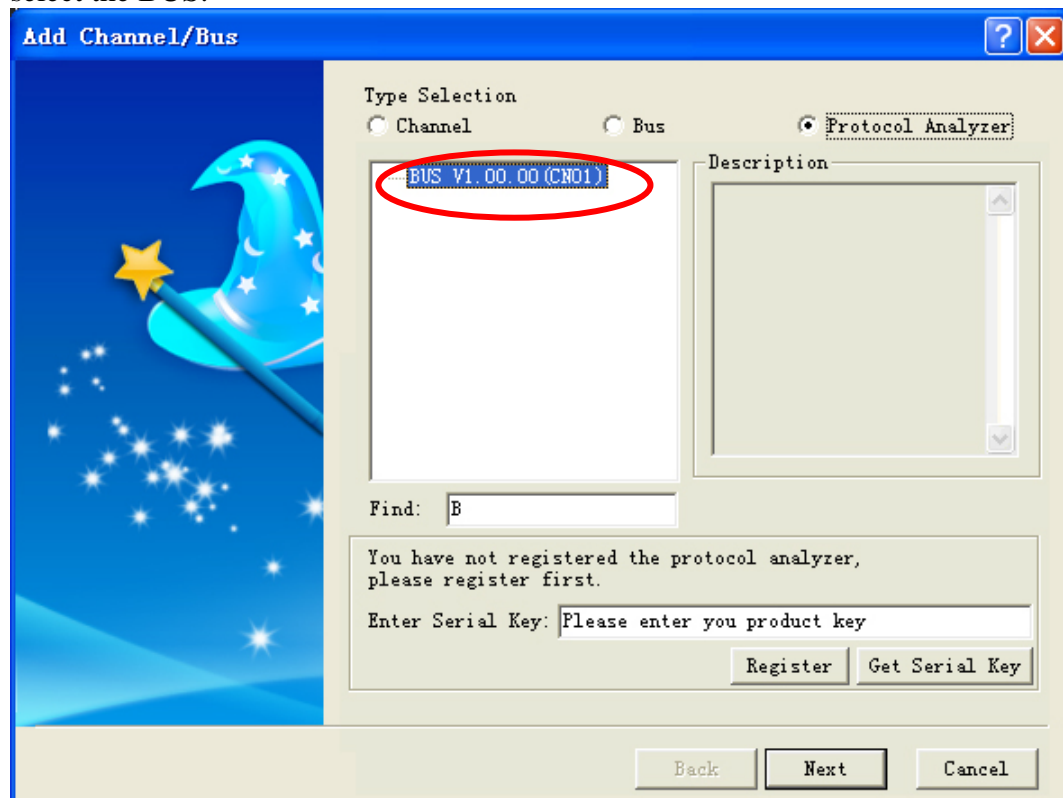
※ Remark1: The registration steps for all protocol analyzers are the same; you can complete the registration by following procedures. Following is an example on how to register the Protocol Analyzer BUS.

※ Remark2: We won't have additional notice for you, when there is any modification of the module specification. If there is some unconformity caused by the module version upgrade, users should take the module software as the standard.

STEP 1. Open the Logic Analyzer and select the **Add Channel/Bus** item on the pull-down menu of the **Sampling(S)** to open the **Add Channel/Bus** dialog box.



STEP 2. Select **Protocol Analyzer** item in the **Add Channel/Bus** dialog box, expand the **Other Type**, and select the **BUS**.





STEP 3. Enter the Serial Key of the BUS under this Model, and then click the **Register**.

Add Channel/Bus

Type Selection
☐ Channel ☐ Bus ☒ Protocol Analyzer

.....BUS V1.00.00 (CN01)

Description

Find: B

You have not registered the protocol analyzer,
please register first.

Enter Serial Key: Please enter you product key

Register Get Serial Key

Back Next Cancel

STEP 4. After the Register is successful, click the **Next**.

Add Channel/Bus

Type Selection
☐ Channel ☐ Bus ☒ Protocol Analyzer

.....BUS V1.00.00 (CN01)

Description

Find: B

Back Next Cancel



2 User Interface

Please refer to the below images to select options of setting **CCIR656 Module**.

CCIR656 Configuration dialog box

Protocol Analyzer Property:

Interface: Set the Interface to 8 BITS and 10BITS.

Sampling Mode: Set the Mode to Rising or Falling.

Decoding Line Number: When the Option is activated, the different Line Segments will be decoded in the Packets; but if the values of all the Line Segments are the same, the Line Segments will not be decoded.

Version: When the Decoding Line Number is activated, the Version is enabled and can be set to CCIR656-3 or CCIR656-4. Notice that the decoded values of the Line Segments may be different for the different Versions.

Scan Mode: When the Decoding Line Number is activated, the Scan Mode is enabled and the Mode can be set to 525-Line or 625-Line. Notice that the decoded values of the Line Segments may be different for the different Scan Modes.

Pin Assignment:









The CCIR656 has two kinds of the Interface, 8 BITS and 10 BITS. When the Version is set to 8 BITS, there are eight Data Lines that are from DB0 to DB7 and one CLK line; when the Version is set to 10 BITS, besides the above-mentioned nine lines, the two Decimal Lines (Decimal [1] and Decimal [2]) should be added.

Protocol Analyzer Format:

Press the **Settings** button to open the Protocol Analyzer Format dialog box. The Color of each Item can be varied as the users' requirements. The Items (Protection, CB, Y, CR, Blanking and Line) can be set as Binary, Decimal, Hexadecimal, ASCII or Default. And the Data Format of these Items (Protection, CB, Y, CR, Blanking and Line) in the Waveform Display Area and Packet List is controlled by the Protocol Analyzer. The default Data Format is controlled by the main program and the Data Format of these items (Protection, CB, Y, CR, Blanking and Line) is the Default.



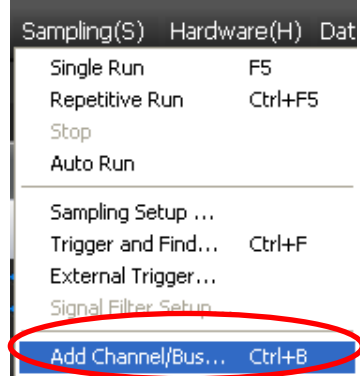
Protocol Analyzer Format

Item	Color	Data Format	Item	Color	Data Format
SAV		Default	CR		Default
Protection		Default	Blanking		Default
CB		Default	EAV		Default
Y		Default	Line		Default

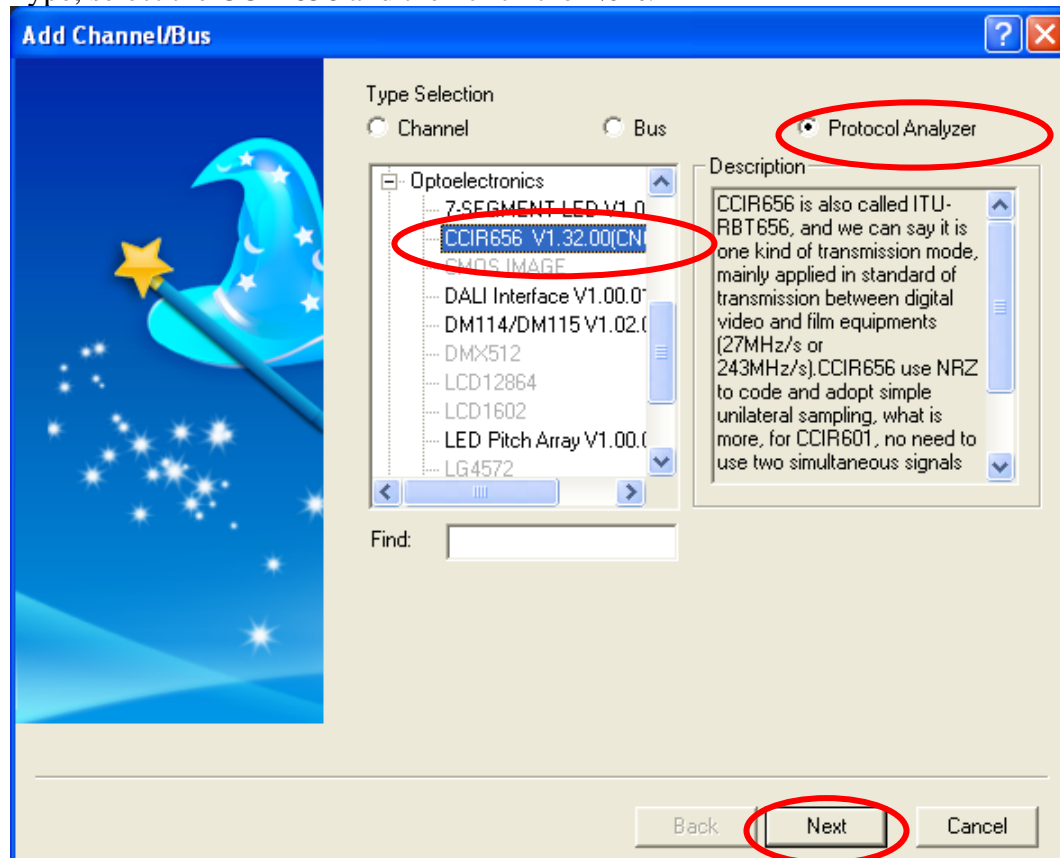
OK Cancel Default

3 Operating Instructions

STEP 1. Select the Add Channel/Bus item on the pull-down menu of the Sampling(S) to open the Add Channel/Bus dialog box.



STEP 2. Select the Protocol Analyzer item in the Add Channel/Bus dialog box, expand the Optoelectronics Type, select the CCIR656 and then click the **Next**.





STEP 3. Open the PROTOCOL ANALYZER CCIR656 dialog box and set the Interface to 8 BITS or 10 BITS.

PROTOCOL ANALYZER CCIR656

Protocol Analyzer Property

Interface: 8 BITS

Sampling Mode: Rising

☒ Decoding Line Number

Version: CCIR656-3

Scan Mode: 525-Line

Protocol Analyzer Format

Settings...

Pin Assignment

DB0: A0 DB4: A4 CLK: B0

DB1: A1 DB5: A5 Decimal[1]: B1

DB2: A2 DB6: A6 Decimal[0]: B2

DB3: A3 DB7: A7

Default Back Next Cancel

STEP 4. Set the Sampling Mode to Rising or Falling.

PROTOCOL ANALYZER CCIR656

Protocol Analyzer Property

Interface: 8 BITS

Sampling Mode: Rising

☒ Decoding Line Number

Version: CCIR656-3

Scan Mode: 525-Line

Protocol Analyzer Format

Settings...

Pin Assignment

DB0: A0 DB4: A4 CLK: B0

DB1: A1 DB5: A5 Decimal[1]: B1

DB2: A2 DB6: A6 Decimal[0]: B2

DB3: A3 DB7: A7

Default Back Next Cancel



STEP 5. Select the **Decoding Line Number**, and set the **Version** to CCIR656-3 or CCIR656-4 and the **Scan Mode** to 525-Line or 625-Line.

PROTOCOL ANALYZER CCIR656

Protocol Analyzer Property

Interface: 8 BITS Sampling Mode: Rising

☒ Decoding Line Number

Version: CCIR656-3 Scan Mode: 525-Line

Pin Assignment

DB0: A0 DB4: A4 CLK: B0

DB1: A1 DB5: A5 Decimal[1]: B1

DB2: A2 DB6: A6 Decimal[0]: B2

DB3: A3 DB7: A7

Settings...

Default Back Next Cancel

STEP 6. Set the Channels for the CCIR656 in the Pin Assignment.

PROTOCOL ANALYZER CCIR656

Protocol Analyzer Property

Interface: 8 BITS Sampling Mode: Rising

☒ Decoding Line Number

Version: CCIR656-3 Scan Mode: 525-Line

Pin Assignment

DB0: A0 DB4: A4 CLK: B0

DB1: A1 DB5: A5 Decimal[1]: B1

DB2: A2 DB6: A6 Decimal[0]: B2

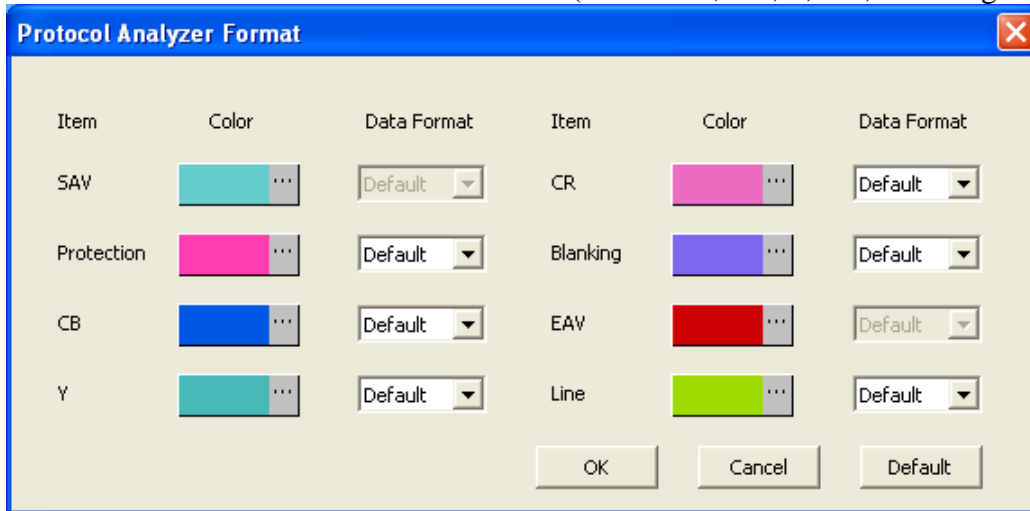
DB3: A3 DB7: A7

Settings...

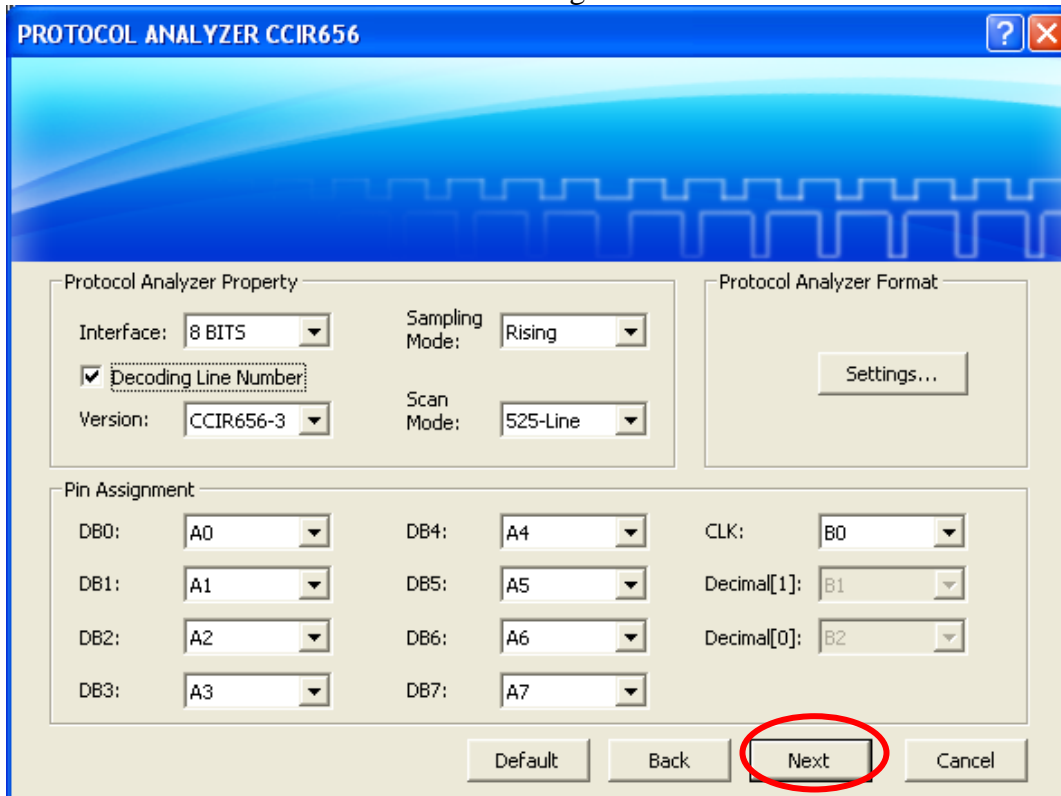
Default Back Next Cancel



STEP 7. Press the **Settings** button to open the Protocol Analyzer Format dialog box, and then set the Color of each Item and the Data Format of the Items (Protection, CB, Y, CR, Blanking and Line).

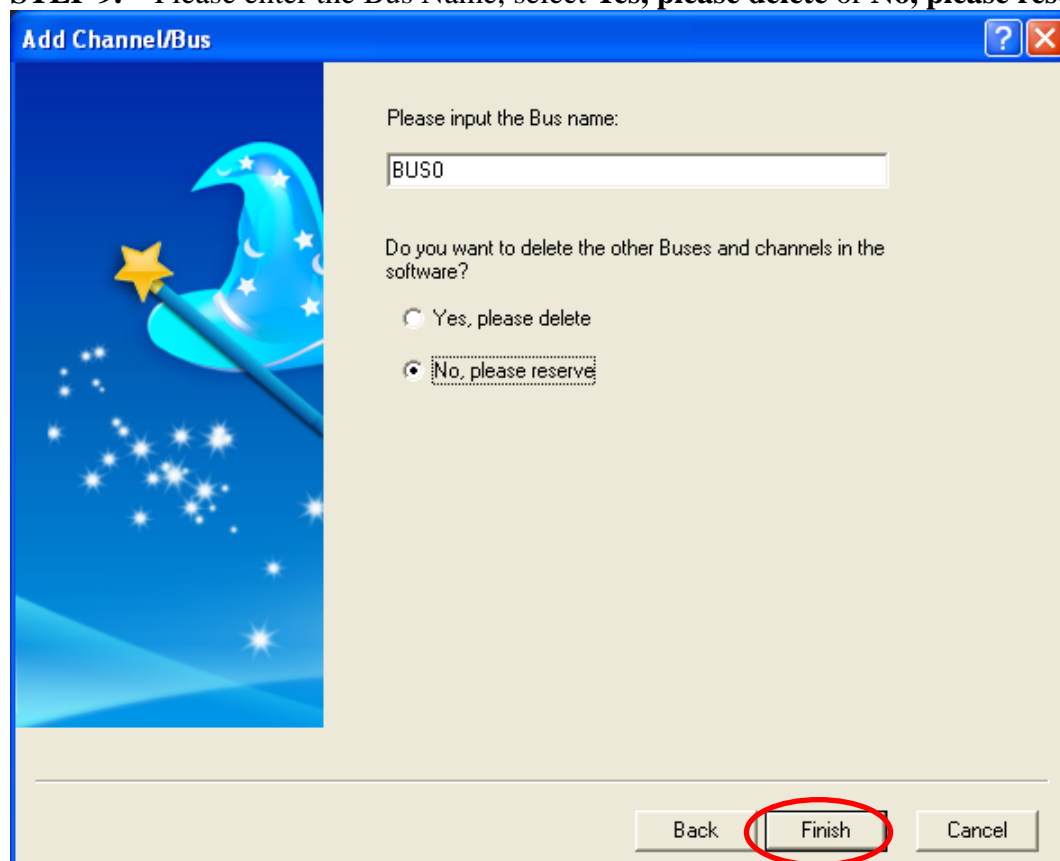


STEP 8. Click the **Next** to finish all settings.



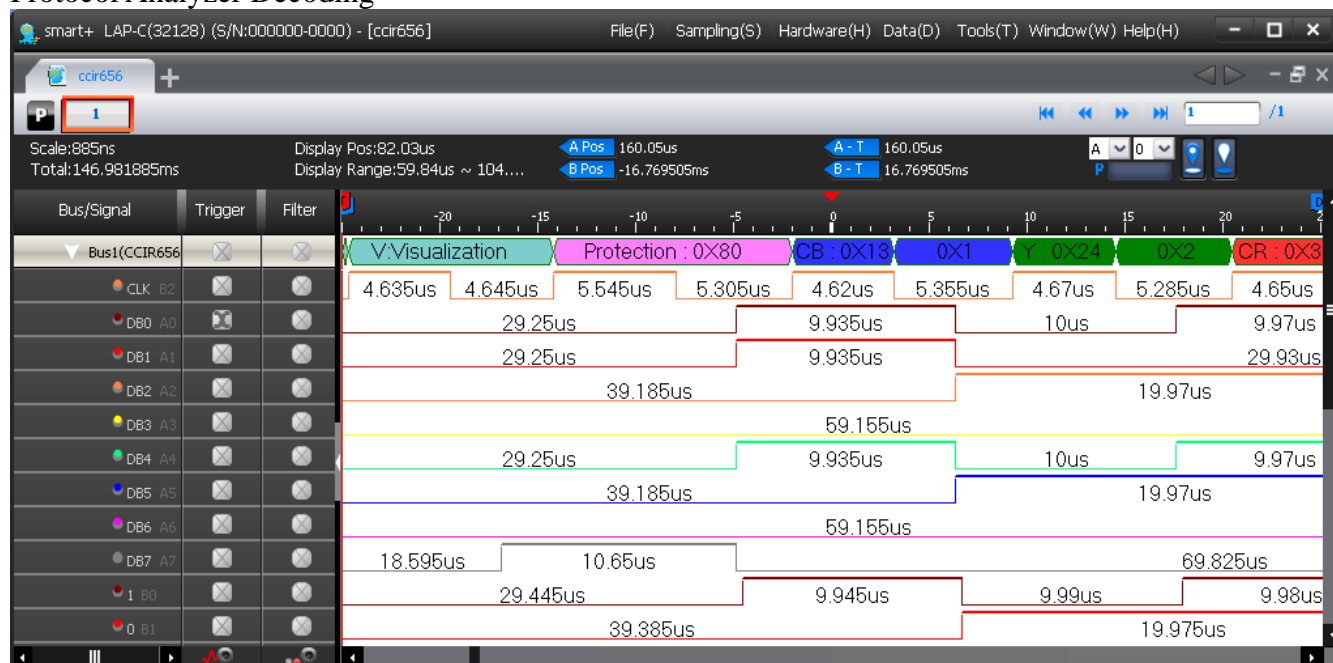


STEP 9. Please enter the Bus Name, select **Yes, please delete** or **No, please reserve** and then press **Finish**.



STEP 10. Following pictures show the completion of the protocol analyzer decoding and the packet list. The trigger condition is set as Either Edge; the memory depth is 128K; the sampling frequency is 200MHz (the sampling frequency should be more than four times higher than the signal to be tested).

Protocol Analyzer Decoding





Packet List

